

✓Docket No.: 0057-2567-2YY

"RESPONSE UNDER 37 C.F.R. § 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP/ART UNIT 2812"

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Yasuo YAMAGUCHI et al.

SERIAL NO.: 09/466,934

FILED: DECEMBER 20, 1999

FOR: SEMICONDUCTOR DEVICE,
METHOD OF MANUFACTURING
SAME AND METHOD OF
DESIGNING SAME

:

: EXAMINER: STEVENSON, A.

:

: GROUP ART UNIT: 2812

[Handwritten signature]

TECHNOLOGY CENTER 2800

DEC 14 2001

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AMENDMENT AFTER FINAL

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action dated July 18, 2001, please amend the above-
identified application as follows:

IN THE CLAIMS

12/21/2001 VVANNALL 00000001 09466934

01 FC:116

400.00 OP

**Please amend the claims as shown in the attachment. A complete set of amended
claims in clean form is shown below.**

Please amend Claims 1, 6, and 23 as follows.

1. (Amended) A semiconductor device having a SOI structure including a
semiconductor substrate, a buried insulation layer and an SOI layer, said semiconductor
device comprising:

*AI
Cont*

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